

Amendments to the Specification:

Please replace the paragraph beginning on page 1, line 18, with the following rewritten paragraph:

In addition to the above-~~described~~described sleep function, the facsimile apparatus also includes a clock adjustment function. The clock adjustment function is used, for example, at the beginning and end of daylight-saving time to set the clock ahead and back, respectively, by one hour. More specifically, with the clock adjustment function, a built-in clock of the facsimile apparatus is automatically advanced one hour at the beginning of day-light saving time. The time adjusted by advancing the clock is output so that the adjusted time is indicated on a display panel. The clock adjustment function is performed by the CPU.

Please replace the paragraph beginning on page 5, line 11, with the following rewritten paragraph:

The user can freely change the monitoring time, based on which the control device is switched to the sleep condition. For example, ~~as the if the~~ monitoring time is set short, the power-saving time can be increased. In other words, the power-saving time is increased by the amount that the monitoring time is decreased.

Please replace the paragraph beginning on page 14, line 11, with the following rewritten paragraph:

When no external condition is detected during the sleep condition of the CPU 1 (S14: NO), and the current time approaches the clock forward or back adjustment time (S15: YES), the I/O monitor 15 starts the power supply to the CPU 1 and the modem 4, by the use of the operational condition switching circuit 15B, contrary to S13 . The CPU 1 is switched to the active condition (S16). In this case, the recovery condition is input as a signal, from the condition inputting circuit 15C, to the operational condition switching circuit 15B, in order to switch the condition of the CPU 1 from the sleep condition to the active condition. The CPU

1 is placed in a condition that the CPU 1 is able to automatically execute the time correction mode switching (S3 and S5) in Fig. 2. The time correction mode switching is instantly performed.

Please replace the paragraph beginning on page 15, line 7, with the following rewritten paragraph:

In S15, when the current time is not the clock forward or back adjustment time (S15: NO) even while the CPU 1 is in the sleep condition, flow returns to S14 where the I/O monitor 15 monitors whether any external condition is input.